

**IN THE**  
**UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Donald E. Steiss, et al.  
APPLICATION NO.: 10/749,912  
FILING DATE: December 30, 2003  
TITLE: Instruction Memory Hierarchy for Embedded Processor  
EXAMINER: Jacob Andrew Petranek  
GROUP ART UNIT: 2183  
ATTY. DKT. NO.: 22347-08261

MAIL STOP AMENDMENT  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

**AMENDMENT A AND RESPONSE**

Sir:

This amendment for the patent application identified above is in response to the Office Action dated April 10, 2006, which set a shortened statutory period for response that expires on July 10, 2006.

Kindly amend this application as indicated herein.